## NP and Computational Intractability

T. M. Murali

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## **Algorithm Design**

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  - Greed.
  - ► Divide-and-conquer.
  - Dynamic programming.
  - Duality.

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  - Local search.
  - Randomization.
- "Anti-patterns"
  - NP-completeness.
  - PSPACE-completeness.

  - Undecidability.

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 $O(n^k)$  algorithm unlikely.  $O(n^k)$  certification algorithm unlikely. No algorithm possible.

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Polynomial time	Probably not
Shortest path	Longest path
Matching	3-D matching
Minimum cut	Maximum cut
2-SAT	3-SAT
Planar four-colour	Planar three-colour
Bipartite vertex cover	Vertex cover
Primality testing	Factoring

### **Problem Classification**

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- However, classification is unclear for a very large number of discrete computational problems.
- ▶ We can prove that these problems are fundamentally equivalent and are manifestations of the same problem!

### **Polynomial-Time Reduction**

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- ▶ Use the notion of reductions.
- ▶ Y is polynomial-time reducible to X  $(Y \leq_P X)$

### **Polynomial-Time Reduction**

- ► Goal is to express statements of the type "Problem X is at least as hard as problem Y."
- ▶ Use the notion of reductions.
- ▶ Y is polynomial-time reducible to X ( $Y \leq_P X$ ) if an arbitrary instance of Y can be solved using a polynomial number of standard operations, plus a polynomial number of calls to a black box that solves problem X.
- $\triangleright$   $Y <_P X$  implies that "X is at least as hard as Y."
- ▶ Such reductions are *Cook reductions*. *Karp reductions* allow only one call to the black box that solves *X*.

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- ▶ Claim: If  $Y \leq_P X$  and X can be solved in polynomial time, then Y can be solved in polynomial time.
- ▶ Contrapositive: If  $Y \leq_P X$  and Y cannot be solved in polynomial time, then X cannot be solved in polynomial time.
- ▶ Informally: If *Y* is hard, and we can show that *Y* reduces to *X*, then the hardness "spreads" to *X*.

## **Reduction Strategies**

- ► Simple equivalence.
- ► Special case to general case.
- Encoding with gadgets.

## **Optimisation versus Decision Problems**

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- So far, we have developed algorithms that solve optimisation problems.
  - Compute the largest flow.
  - Find the closest pair of points.
  - Find the schedule with the least completion time.
- ▶ Now, we will focus on *decision versions* of problems, e.g., is there a flow with value at least *k*, for a given value of *k*.

- ▶ Given an undirected graph G(V, E), a subset  $S \subseteq V$  is an independent set if no two vertices in S are connected by an edge.
- ▶ Given an undirected graph G(V, E), a subset  $S \subseteq V$  is a *vertex cover* if every edge in E is incident on at least one vertex in S.

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**QUESTION:** Does *G* contain an independent set

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- ▶ Demonstrate simple equivalence between these two problems.
- ▶ Claim: S is an independent set in G iff V S is a vertex cover in G.
- ▶ Claim: INDEPENDENT SET  $\leq_P$  VERTEX COVER and VERTEX COVER  $\leq_P$  INDEPENDENT SET.

### **Vertex Cover and Set Cover**

- ▶ INDEPENDENT SET is a "packing" problem: pack as many vertices as possible, subject to constraints (the edges).
- ► VERTEX COVER is a "covering" problem: cover all edges in the graph with as few vertices as possible.
- ▶ There are more general covering problems.

### Set Cover

**INSTANCE:** A set U of n elements, a collection  $S_1, S_2, \ldots, S_m$  of subsets of U, and an integer k.

**QUESTION:** Is there a collection of  $\leq k$  sets in the collection whose union is U?

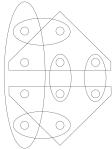


Figure 8.2 An instance of the Set Cover Problem

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- ▶ Input to VERTEX COVER is an undirected graph G(V, E) with n vertices.
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  - ▶ for each vertex  $i \in V$ , create a set  $S_i \subseteq U$  pf the edges incident on i.
- ► Claim: *U* can be covered with fewer than *k* subsets iff *G* has a vertex cover with at most *k* nodes.

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- ▶ We are given a set  $X = \{x_1, x_2, ..., x_n\}$  of n Boolean variables.
- ► Each variable can take the value 0 or 1.
- ▶ A *term* is a variable  $x_i$  or its negation  $\overline{x_i}$ .
- ▶ A *clause* of *length I* is a disjunction of *I* distinct terms  $t_1 \lor t_2 \lor \cdots t_I$ .
- ▶ A truth assignment for X is a function  $\nu: X \to \{0,1\}$ .
- ▶ An assignment *satisfies* a clause *C* if it causes *C* to evaluate to 1 under the rules of Boolean logic.
- ▶ An assignment *satisfies* a collection of clauses  $C_1, C_2, ..., C_k$  if it causes  $C_1 \land C_2 \land \cdots \land C_k$  to evaluate to 1.
  - $\triangleright$   $\nu$  is a satisfying assignment with respect to  $C_1, C_2, \dots C_k$ .
  - $\triangleright$  set of clauses  $C_1, C_2, \dots C_k$  is satisfiable.

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### SAT and 3-SAT

Satisfiability Problem (SAT)

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3-Satisfiability Problem (3-SAT)

**INSTANCE:** A set of clauses  $C_1, C_2, ..., C_k$  each of length 3 over a set  $X = \{x_1, x_2, ..., x_n\}$  of n variables.

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- ▶ SAT and 3-SAT are fundamental combinatorial search problems.
- ▶ We have to make *n* independent decisions (the assignments for each variable) while satisfying a set of constraints.
- ▶ Satisfying each constraint in isolation is easy, but we have to make our decisions so that all constraints are satisfied simultaneously.

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- ▶ We want to prove  $3\text{-SAT} \leq_P \text{INDEPENDENT SET}$ .
- ► Two ways to think about 3-SAT:
  - 1. Make an independent 0/1 decision on each variable and succeed if we achieve one of three ways in which to satisfy each clause.
  - 2. Choose (at least) one term from each clause. Find a truth assignment that causes each chosen term to evaluate to 1. Ensure that no two terms selected *conflict*, i.e., select  $x_i$  and  $\overline{x_i}$ .

## **Proving 3-SAT** $\leq_P$ **Independent Set**

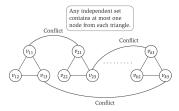


Figure 8.3 The reduction from 3-SAT to Independent Set.

- We are given an instance of 3-SAT with k clauses of length three over n variables.
- ▶ Construct a graph G(V, E) with 3k nodes.
  - ▶ For each clause  $C_i$ ,  $1 \le i \le k$ , add a triangle of three nodes  $v_{i1}$ ,  $v_{i2}$ ,  $v_{i3}$  and three edges to G.
  - ▶ Label each node  $v_{ij}$ ,  $1 \le j \le 3$  with the *j*th term in  $C_i$ .

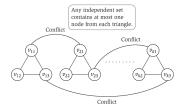


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  - Add an edge between each pair of nodes whose labels correspond to terms that conflict.

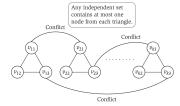


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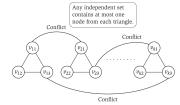


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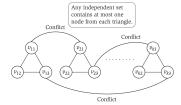


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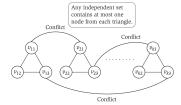


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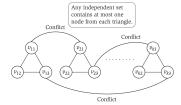


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- ▶ Independent set of size  $\geq k \rightarrow$  satisfiable assignment: the size of this set is k. How do we construct a satisfying truth assignment from the nodes in the independent set?

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- ▶ We have shown

3-SAT  $\leq_P$  INDEPENDENT SET  $\leq_P$  VERTEX COVER  $\leq_P$  SET COVER

## Finding vs. Certifying

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- We draw a contrast between finding a solution and checking a solution (in polynomial time).
- ▶ Since we have not been able to develop efficient algorithms to solve many decision problems, let us turn our attention to whether we can check if a proposed solution is correct.

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- $\triangleright \mathcal{P}$ : set of problems X for which there is a polynomial time algorithm.

#### **Efficient Certification**

- ▶ A "checking" algorithm for a decision problem *X* has a different structure from an algorithm that solves *X*.
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- ▶ An algorithm B is an *efficient certifier* for a problem X if
  - 1. B is a polynomial time algorithm that takes two inputs s and t and
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- ▶ Certifier's job is to take a candidate short proof (t) that  $s \in X$  and check in polynomial time whether t is a correct proof.
- Certifier does not care about how to find these proofs.

$$\mathcal{NP}$$

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$${\mathcal P}$$
 vs.  ${\mathcal N}{\mathcal P}$ 

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- ▶ Are there any  $\mathcal{NP}$ -Complete problems?
  - 1. Perhaps there are two problems  $X_1$  and  $X_2$  in  $\mathcal{NP}$  such that there is no problem  $X \in \mathcal{NP}$  where  $X_1 \leq_P X$  and  $X_2 \leq_P X$ .
  - 2. Perhaps there is a sequence of problems  $X_1, X_2, X_3, \ldots$  in  $\mathcal{NP}$ , each strictly harder than the previous one.

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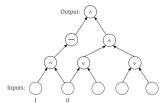


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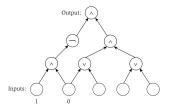


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#### CIRCUIT SATISFIABILITY

**INSTANCE:** A circuit *K*.

**QUESTION:** Is there a truth assignment to the inputs that causes the output to have value 1?

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- ▶  $s \in X$  iff there is an assignment of the input bits of K that makes K satisfiable.

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- ▶ s encodes the graph G with  $\binom{n}{2}$  bits.
- t encodes the independent set with *n* bits.
- Certifier needs to check if
  - 1. at least two bits in t are set to 1 and
  - 2. no two bits in t are set to 1 if they form the ends of an edge (the corresponding bit in s is set to 1).

Suppose G contains three nodes u, v, and w with v connected to u and w.

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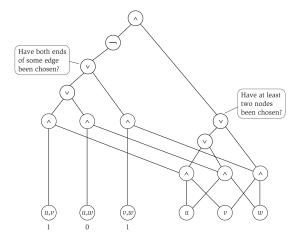


Figure 8.5 A circuit to verify whether a 3-node graph contains a 2-node independent set.

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- ▶ If we use Karp reductions, we can refine the strategy:
  - 1. Prove that  $X \in \mathcal{NP}$ .
  - 2. Select a problem Y known to be  $\mathcal{NP}$ -Complete.
  - 3. Consider an arbitrary instance  $s_Y$  of problem Y. Show how to construct, in polynomial time, an instance  $s_X$  of problem X such that
    - (a) If  $s_Y \in Y$ , then  $s_X \in X$  and
    - (b) If  $s_X \in X$ , then  $s_Y \in Y$ .