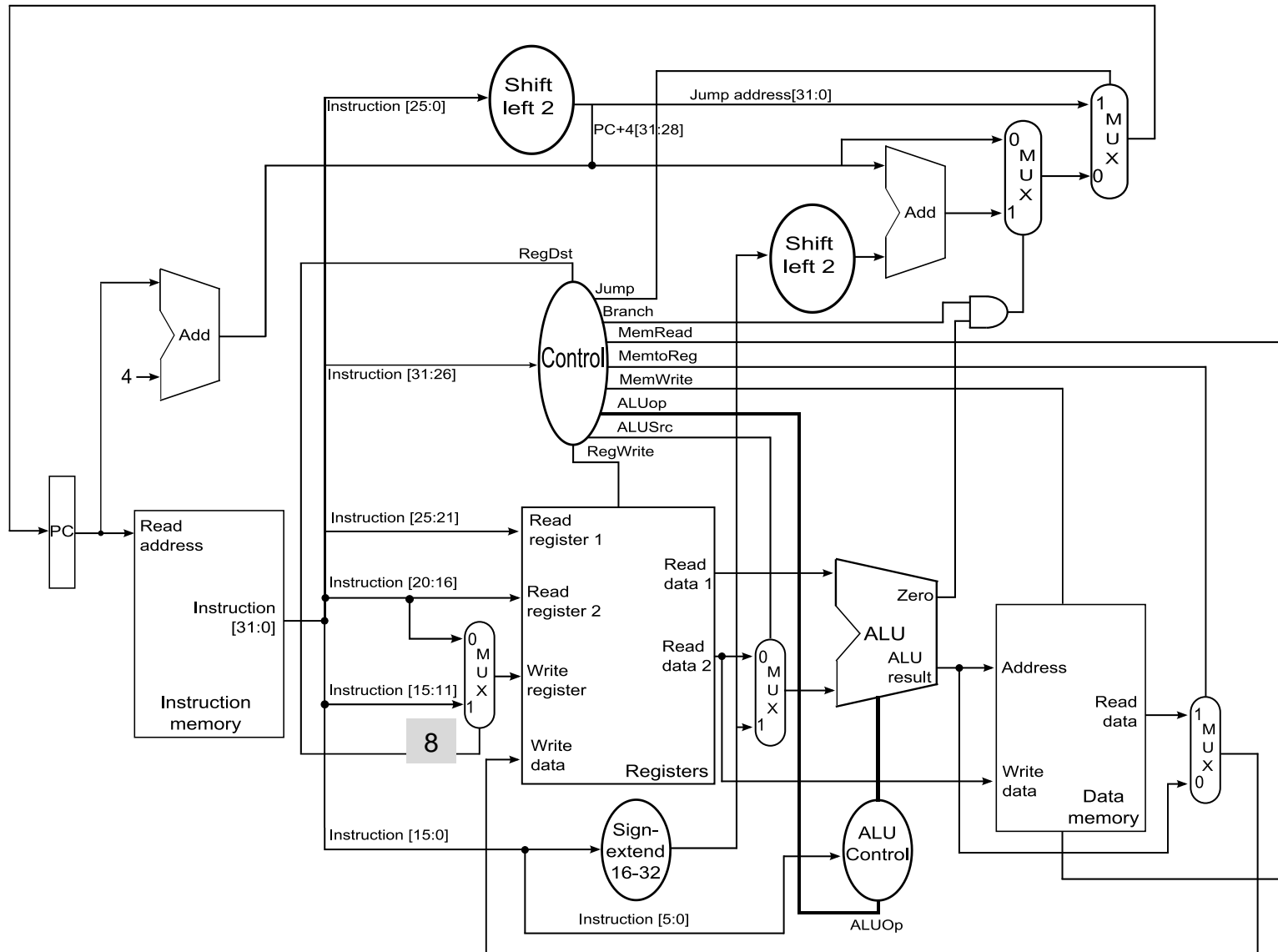The single-cycle MIPS32 datapath:



Supported instructions:        add, sub, and, or, slt, lw, sw, beq, j

The preliminary pipelined MIPS32 datapath: