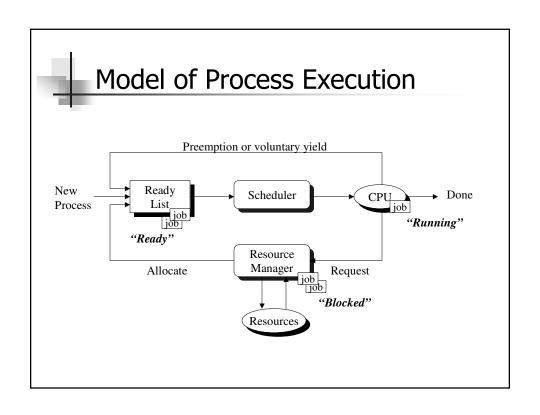


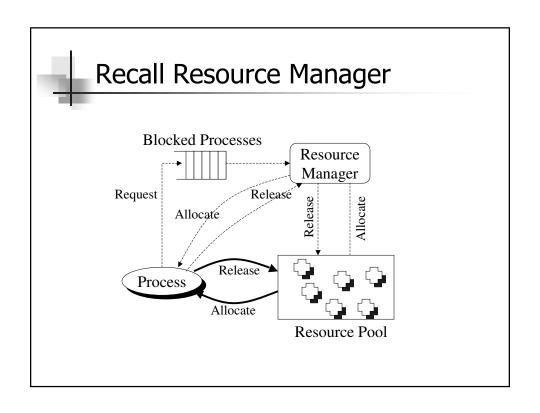
Chapter 7: Scheduling

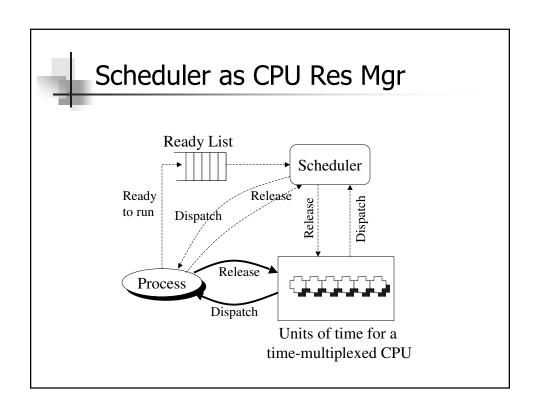


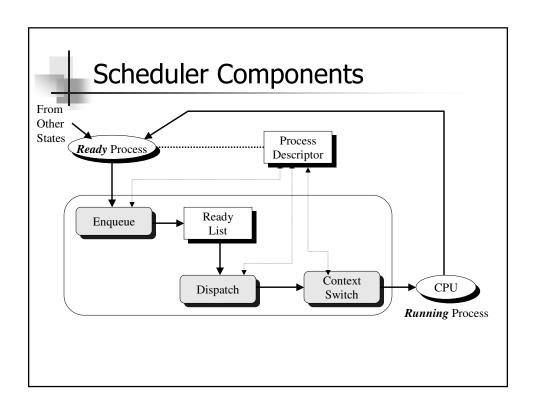
Process Scheduler

- Why do we even <u>need</u> to a process scheduler?
 - In simplest form, CPU must be shared by
 - OS
 - Application
 - In reality, [multiprogramming]
 - OS : many separate pieces (processes)
 - Many Applications
- Scheduling [Policy] addresses...
 - When to remove a process from CPU?
 - Which ready process to allocate the CPU to ?







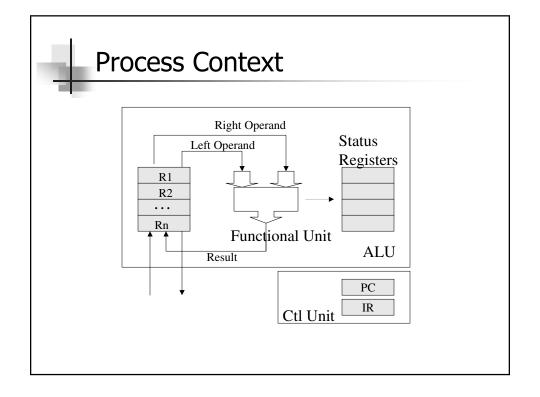


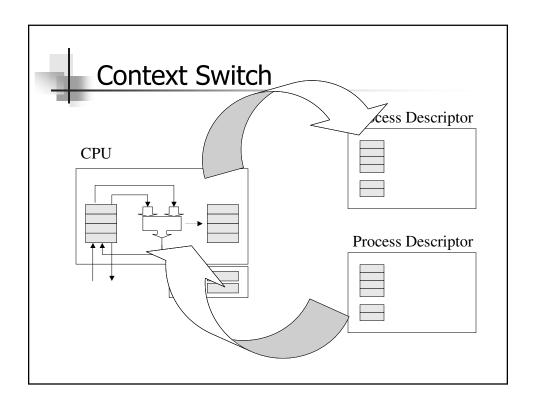


Context Switch

- Processes are switched out using <u>Context Switching</u>
- Context Switch:
 - Save pertinent info for current process
 - PC, Register, Status, etc.
 - **Update** PC, Register, Status, etc.
 - with info for process selected to run
- Switching User Process
 - 2 Context switches (CTX)

Process 1 running
CTX
Dispatcher: selects next process
CTX
Process 2 running







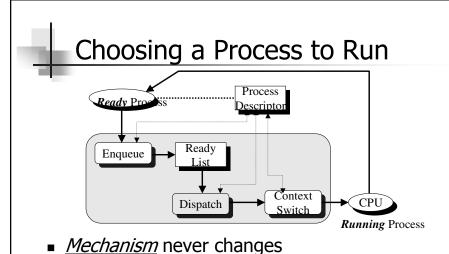
Invoking the Scheduler

- Need a <u>mechanism</u> to call the scheduler
- Voluntary call
 - Process blocks itself
 - Calls the scheduler
- Involuntary call
 - External force (interrupt) blocks the process
 - Calls the scheduler



Contemporary Scheduling

- Involuntary CPU sharing timer interrupts
 - <u>Time quantum</u> determined by interval timer – usually fixed size for every process using the system
 - Sometimes called the *time slice length*



- select a process from the ready list
- Different policies for different requirements



Policy Considerations

- Policy can control/influence:
 - CPU utilization
 - Average time a process waits for service
 - Average amount of time to complete a job
- Could strive for any of:
 - Equitability
 - Favor very short or long jobs
 - Meet priority requirements
 - Meet deadlines



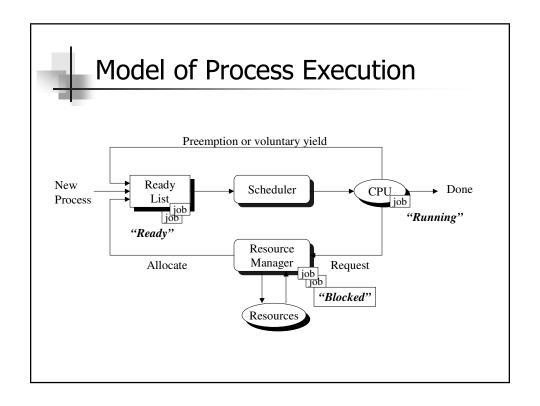
Optimal Scheduling

- Suppose the scheduler knows each process p_i 's service time, $\tau(p_i)$ -- or it can estimate each $\tau(p_i)$:
- Policy can optimize on any criteria, e.g.,
 - CPU utilization
 - Waiting time
 - Deadline
- To find an *optimal schedule*:
 - Have a finite, fixed # of p_i
 - Know τ(p_i) for each p_i
 - Enumerate all schedules, then choose the best



However ...

- The τ(p_i) are almost certainly just estimates
- General algorithm to choose optimal schedule is O(n²)
- Other processes may arrive while these processes are being serviced
- Usually, optimal schedule is only a <u>theoretical benchmark</u> – scheduling policies try to <u>approximate</u> an optimal schedule





Selection Strategies

- Motivation
 - To "optimize" some aspect of system behavior
- Considerations
 - Priority of process
 - External : assigned
 - Internal : aging
 - Fairness: no starvation
 - Overall Resource Utilization

...



Selection Strategies...

- Considerations...
 - Turnaround time
 - Average time / job
 - Throughput
 - Jobs / time unit
 - Response time
 - System availability
 - Deadlines

Talking About Scheduling ...

- Let $P = \{p_i \mid 0 \le i < n\} = \text{set of processes}$
- Let S(p_i) ∈ {running, ready, blocked}
- Let $\tau(p_i)$ = Time process needs to be in running state (the <u>service time</u>)
- Let $W(p_i)$ = Time p_i is in ready state before <u>first</u> transition to running (<u>wait time</u>)
- Let T_{TRnd}(p_i) = Time from p_i first enter ready to last exit ready (*turnaround time*)
- Batch <u>Throughput rate</u> = inverse of avg T_{TRnd}
- Timesharing response time = $W(p_i)$



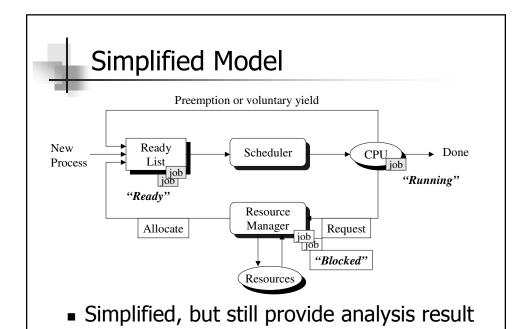
Definition & Terms

- Time Quantum
 - Amount of time between timer interrupts
 - Also called Time Slice
- Service Time τ (P_i)
 - Amount of time process needs to be in Running state (acquired CPU) before it is completed
- Wait Time W (P_i)
 - Time a process spends waiting in the Ready state before its *first* transition to the Running state



Definition & Terms...

- Turnaround Time T (P_i)
 - Amount of time between moment process first enters Ready state and the moment the process exits Running state for the last time (completed)
- Service time, Wait time & Turnaround time are measurable metrics used to compare scheduling algorithms



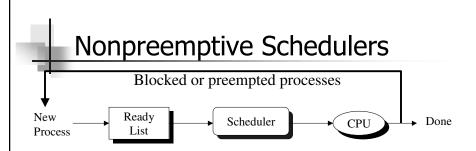
Easy to analyze performance



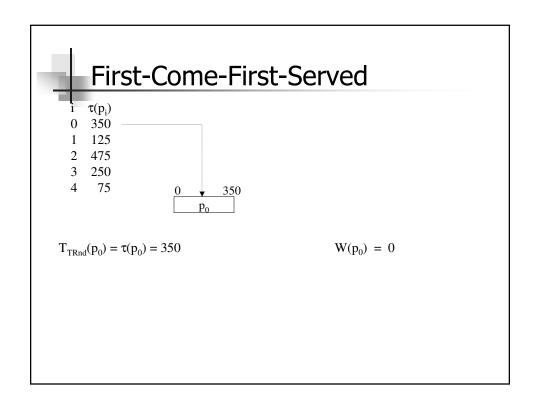
Classes of Scheduling Algorithms

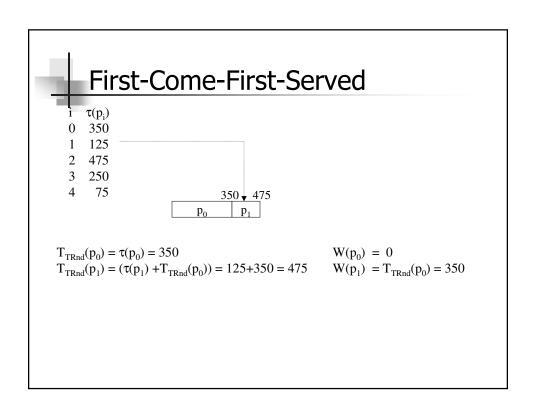
- 2 major classes
 - Non-preemptive
 - Run to completion
 - Preemptive
 - Process with highest priority always gets CPU

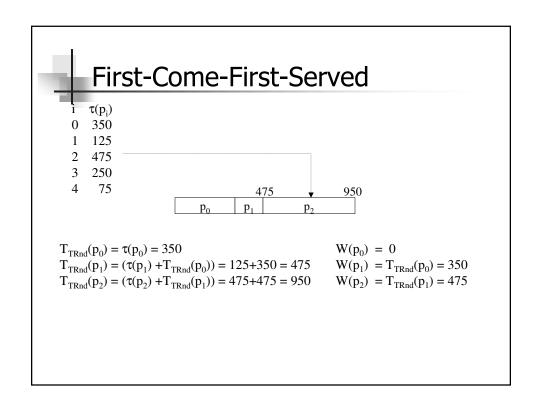
Recall: Several ways to establish priority

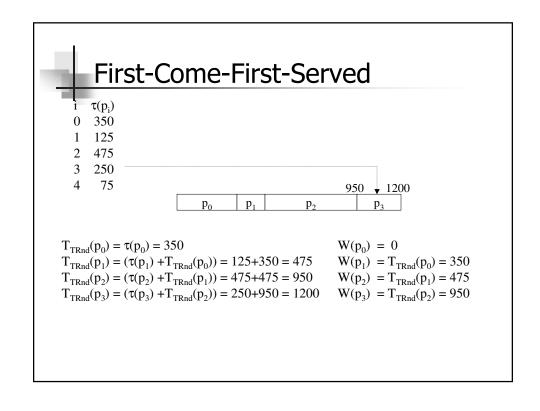


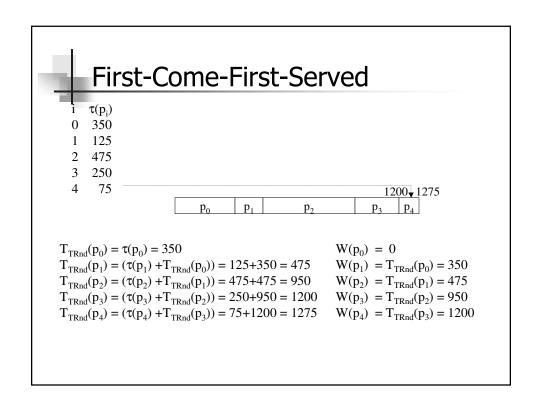
- Try to use the simplified scheduling model
- Only consider <u>running</u> and <u>ready</u> states
- Ignores time in <u>blocked</u> state:
 - "New process created when it enters ready state"
 - "Process is destroyed when it enters blocked state"
 - Really just looking at "small phases" of a process

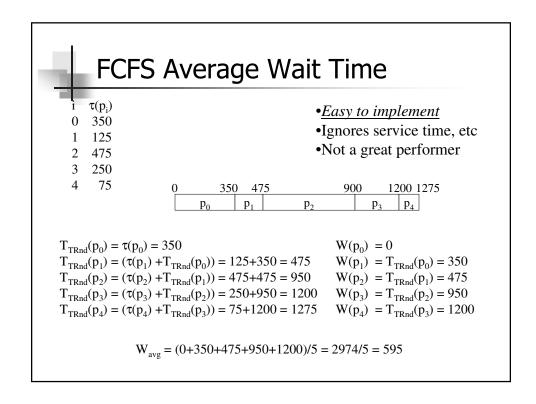








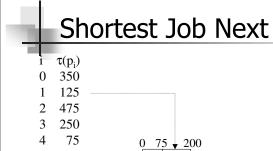




Shortest Job Next 1 τ(p_i) 0 350 1 125 2 475 3 250 4 75 0 75 P₄

$$T_{TRnd}(p_4) = \tau(p_4) = 75$$

$$W(p_4) = 0$$



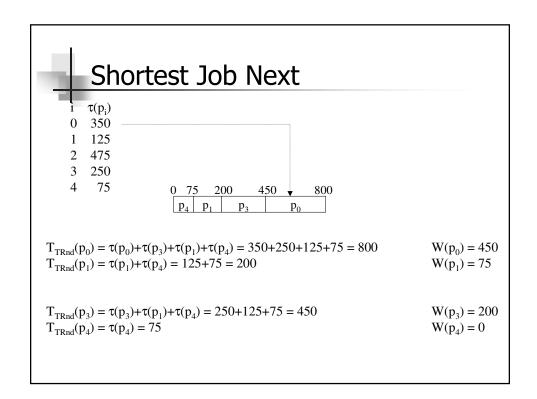
$$T_{TRnd}(p_1) = \tau(p_1) + \tau(p_4) = 125 + 75 = 200$$

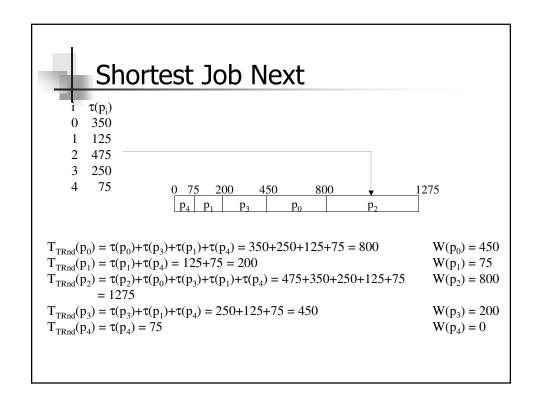
 $p_4 p_1$

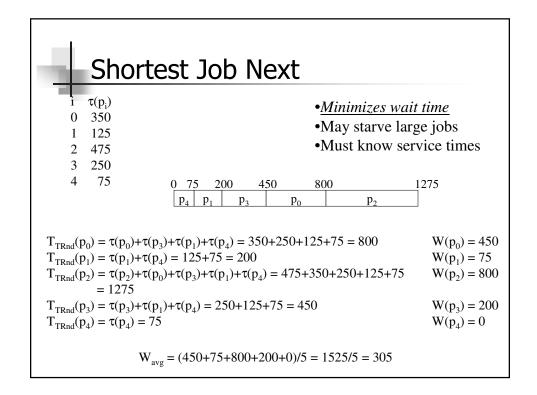
$$W(p_1) = 75$$

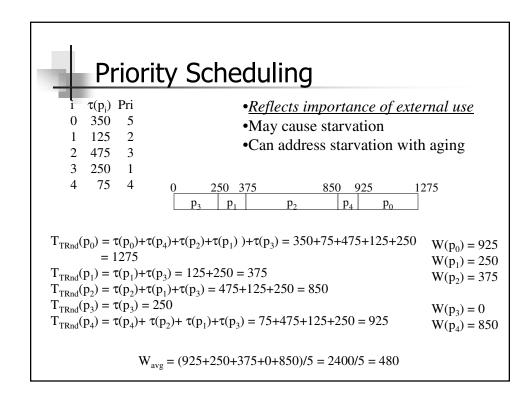
$$T_{TRnd}(p_4) = \tau(p_4) = 75$$

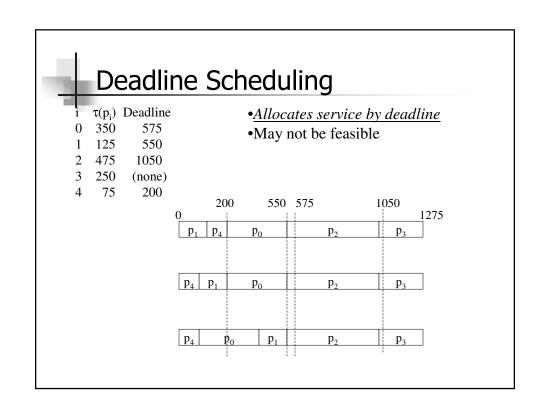
$$W(p_4) = 0$$





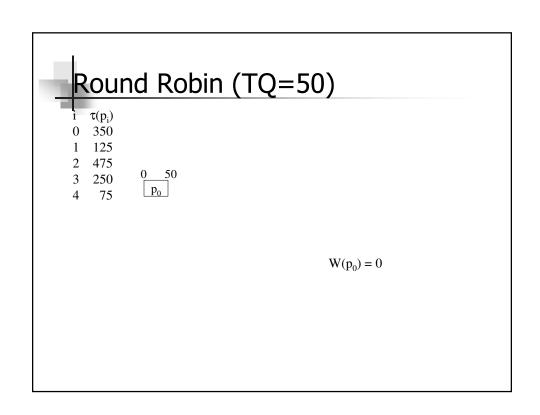






Preemptive Schedulers Preemption or voluntary yield New Process Ready List Scheduler CPU Done

- Highest priority process is guaranteed to be running at all times
 - Or at least at the beginning of a time slice
- Dominant form of contemporary scheduling
- But complex to build & analyze



```
Round Robin (TQ=50)

1 \tau(p_i)
0 350
1 125
2 475
3 250 0 100
4 75 p_0 p_1

W(p_0) = 0
W(p_1) = 50
```

```
Round Robin (TQ=50)

i \tau(p_i)
0 350
1 125
2 475
3 250
4 75

w(p_0) = 0
w(p_1) = 50
w(p_2) = 100
```

```
Round Robin (TQ=50)

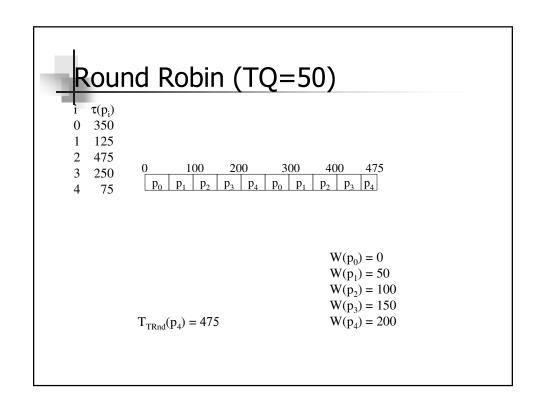
1 \tau(p_i)
0 350
1 125
2 475
3 250
4 75

w(p_0) = 0
w(p_1) = 50
w(p_2) = 100
w(p_3) = 150
```

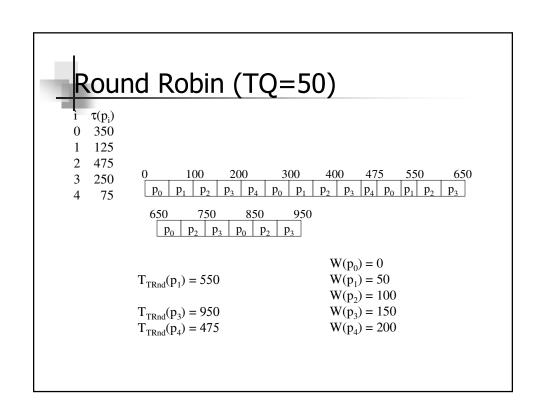
```
Round Robin (TQ=50)

| \tau(p_i) | 0 350 | 1 125 | 2 475 | 3 250 | 0 100 200 | | p_0 | p_1 | p_2 | p_3 | p_4 | | p_4 | p_5 | p_6 | p_
```

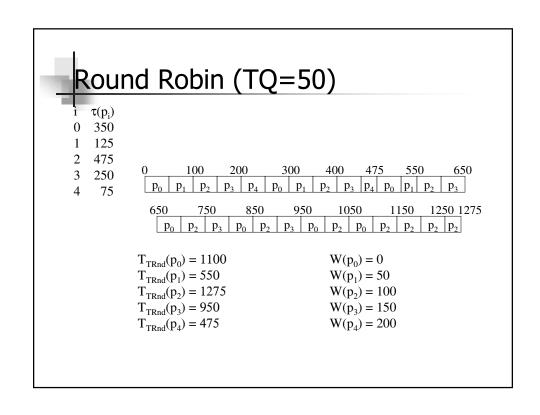
```
Round Robin (TQ=50)
  \tau(\boldsymbol{p}_i)
0 350
  125
  475
              100
                     200
                            300
3 250
         75
                                  W(p_0) = 0
                                   W(p_1) = 50
                                   W(p_2) = 100
                                  W(p_3) = 150
                                  W(p_4) = 200
```



Round Robin (TQ=50) $\tau(\boldsymbol{p}_i)$ 350 125 475 200 300 400 475 3 250 75 $W(p_0) = 0$ $T_{TRnd}(p_1) = 550$ $W(p_1) = 50$ $W(p_2) = 100$ $W(p_3) = 150$ $T_{TRnd}(p_4) = 475$ $W(p_4) = 200$



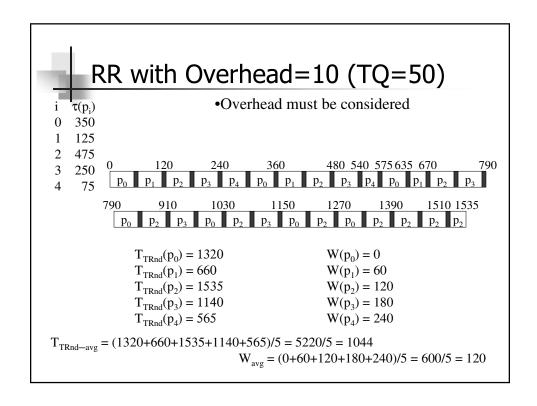
```
Round Robin (TQ=50)
   \tau(\boldsymbol{p}_i)
   350
    125
   475
                                  200
                                              300
                                                       400
3
   250
                oxed{p_0} oxed{p_1} oxed{p_2} oxed{p_2} oxed{p_3} oxed{p_4} oxed{p_0} oxed{p_1} oxed{p_2} oxed{p_2} oxed{p_3} oxed{p_4} oxed{p_0} oxed{p_1} oxed{p_2} oxed{p_3}
     75
                  T_{TRnd}(p_0) = 1100
                                                        W(p_0) = 0
              T_{TRnd}(p_1) = 550
                                                        W(p_1) = 50
                                                        W(p_2) = 100
              T_{TRnd}(p_3) = 950
                                                        W(p_3) = 150
              T_{TRnd}(p_4) = 475
                                                        W(p_4) = 200
```

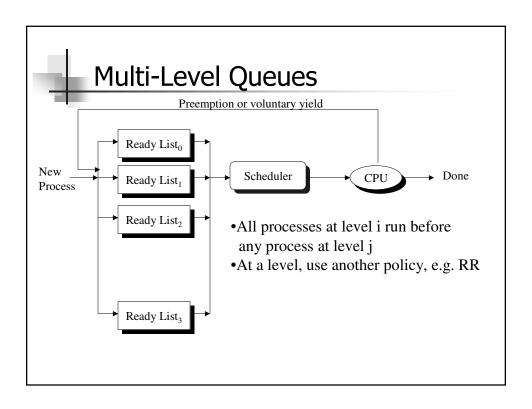


```
Round Robin (TQ=50)

    Equitable

      \tau(p_i)
     350
                            •Most widely-used
      125
                            •Fits naturally with interval timer
      475
                       100
                                               400
   3
      250
                                             p_2 \mid p_3 \mid p_4 \mid p_0 \mid p_1 \mid p_2 \mid p_3
                    p_1 \mid p_2 \mid
       75
                 T_{TRnd}(p_0) = 1100
                                               W(p_0) = 0
              T_{TRnd}(p_1) = 550
                                               W(p_1) = 50
                                               W(p_2) = 100
              T_{TRnd}(p_2) = 1275
                                               W(p_3) = 150
              T_{TRnd}(p_3) = 950
              T_{TRnd}(p_4) = 475
                                               W(p_4) = 200
T_{TRnd-avg} = (1100+550+1275+950+475)/5 = 4350/5 = 870
                                W_{avg} = (0+50+100+150+200)/5 = 500/5 = 100
```





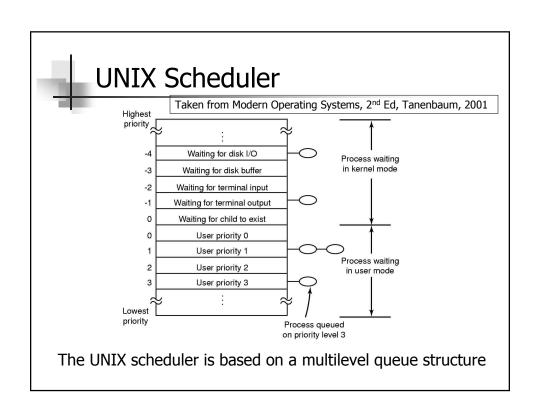
Contemporary Scheduling

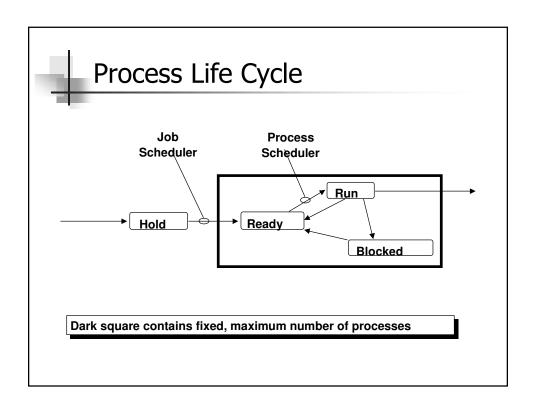
- Involuntary CPU sharing -- timer interrupts
 - <u>Time quantum</u> determined by interval timer -usually fixed for every process using the system
 - Sometimes called the *time slice length*
- *Priority*-based process (job) selection
 - Select the highest priority process
 - Priority reflects policy
- With <u>preemption</u>
- Usually a variant of *Multi-Level Queues*



BSD 4.4 Scheduling

- Involuntary CPU Sharing
- Preemptive algorithms
- 32 Multi-Level Queues
 - Queues 0-7 are reserved for system functions
 - Queues 8-31 are for user space functions
 - nice influences (but does not dictate) queue level





Job and Process Scheduler

Job Scheduler

- Controls when jobs will be allowed to contend the CPU
- Most popular techniques

FIFO First in, first out

SJF Shortest job first

Process Scheduler

- Controls when individual jobs (processes) will actually get the CPU
- · Only interesting in multi-programming
- Most popular technique is <u>Round Robin</u>
 - Give each process one time slice in turn until complete



Turnaround and Weighted Turnaround Time

Let: N be number of jobs

A_i be arrival time of i-th job

F_i be finish time of i-th job

Turnaround time for ith job:

 $T_i = F_i - A_i$

Average turnaround time for ith job:

 $T = \sum T_i / N$

Weighted turnaround time for ith job:

 $WT_i = (F_i - A_i) / (Service-time)_i$

Average Weighted Turnaround time:

 $WT = \Sigma WT_i / N$



Processor Sharing (PS) "Theoretical" Scheduling Algorithm

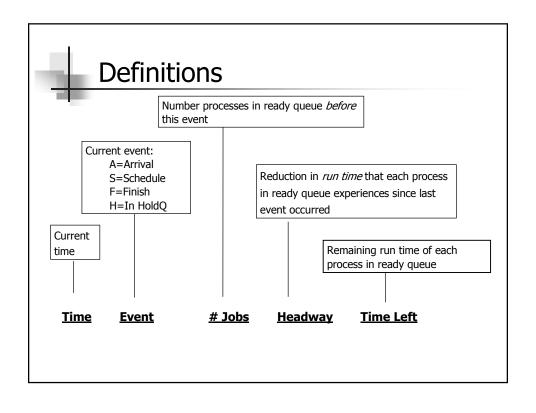
- Limit of RR as time quantum goes to zero.
- Like giving each CPU cycle to a different process, in round robin fashion.
- N processes scheduled by PS
 - Each job runs on dedicated *N*-fold slower CPU.
 - Thus, READY = RUNNING.
- CPU Time "shared" equally among processes



Multiprogramming FIFO Job Scheduling

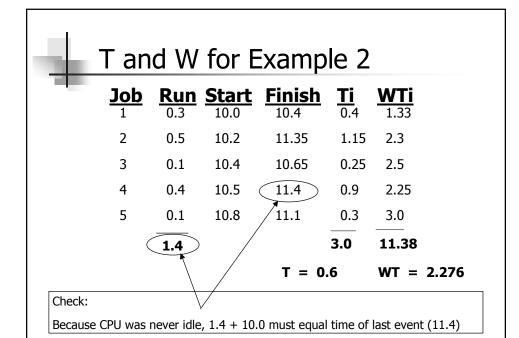
Processor Sharing Process Scheduling

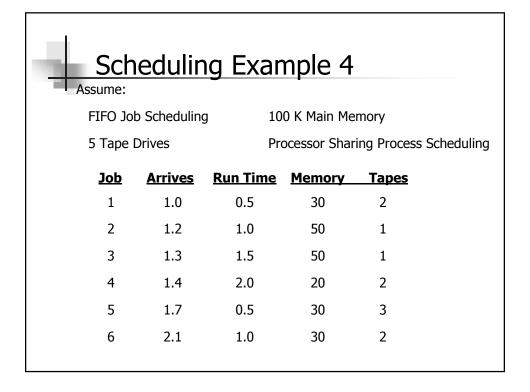
| <u>Job</u> | <u>Arrives</u> | Run Time |
|------------|----------------|-----------------|
| 1 | 10.0 | 0.3 |
| 2 | 10.2 | 0.5 |
| 3 | 10.4 | 0.1 |
| 4 | 10.5 | 0.4 |
| 5 | 10.8 | 0.1 |
| | | |



| Exa | ample | e 2 Co | ontinue | ed | |
|-------------|--------------|--------|----------------|-------------|-------------|
| <u>Time</u> | Event | # Jobs | <u>Headway</u> | <u>Time</u> | <u>Left</u> |
| 10.0 | 1 A,S | | | 1 | 0.3 |
| 10.2 | 2 A,S | 1 | 0.2 | 1 | 0.1 |
| | | | | 2 | 0.5 |
| 10.4 | 1 F | 2 | 0.1 | 2 | 0.4 |
| | 3 A,S | | | 3 | 0.1 |
| 10.5 | 4 A,S | 2 | 0.05 | 2 | 0.35 |
| | | | | 3 | 0.05 |
| | | | | 4 | 0.4 |
| 10.65 | 3 F | 3 | 0.05 | 2 | 0.3 |
| | | | | 4 | 0.35 |

| Exar | mple | 2 Cor | ntinued. | |
|-------------|--------------|--------|----------------|-----------|
| <u>Time</u> | Event | # Jobs | <u>Headway</u> | Time Left |
| 10.8 | 5 A,S | 2 | 0.075 | 2 0.225 |
| | | | | 4 0.275 |
| | | | | 5 0.1 |
| 11.1 | 5 F | 3 | 0.1 | 2 0.125 |
| | | | | 4 0.175 |
| 11.35 | 2 F | 2 | 0.125 | 4 0.05 |
| 11.40 | 4 F | 1 | 0.05 | |
| | | | | |
| | | | | |
| | | | | |





| Exa | mple | e 4 C | ontir | nue | d | |
|-----|-------|--------|-------|-----|-------|-----------|
| | | # Jobs | | | Tapes | Time Left |
| 1.0 | 1 A,S | | | 70 | 3 | 1 0.5 |
| 1.2 | 2 A,S | 1 | 0.2 | 20 | 2 | 1 0.3 |
| | | | | | | 2 1.0 |
| 1.3 | 3 A,H | 2 | 0.05 | 20 | 2 | 1 0.25 |
| | | | | | | 2 0.95 |
| 1.4 | 4 A,S | 2 | 0.05 | 0 | 0 | 1 0.2 |
| | | | | | | 2 0.9 |
| | | | | | | 4 2.0 |
| 1.7 | 5 A,H | 3 | 0.1 | 0 | 0 | 1 0.1 |
| | | | | | | 2 0.8 |
| | | | | | | 4 1.9 |
| 2.0 | 1 F | 3 | 0.1 | 30 | 2 | 2 0.7 |
| | | | | | | 4 1.8 |

| Example 4 Continued | | | | | | | | |
|---------------------|--------------|--------|-------------|-----|--------------|-----------|--|--|
| <u>Time</u> | <u>Event</u> | # Jobs | <u>HWay</u> | MM | <u>Tapes</u> | Time Left | | |
| 2.1 | 6 A,S | 2 | 0.05 | 0 | 0 | 2 0.65 | | |
| | | | | | | 4 1.75 | | |
| | | | | | | 6 1.0 | | |
| 4.05 | 2 F | 3 | 0.65 | 50 | 1 | 4 1.1 | | |
| | 3 S | | | 0 | 0 | 6 0.35 | | |
| | | | | | | 3 1.5 | | |
| 5.1 | 6 F | 3 | 0.35 | 30 | 2 | 4 0.75 | | |
| | | | | | | 3 1.15 | | |
| 6.6 | 4 F | 2 | 0.75 | 50 | 4 | 3 0.4 | | |
| | 5 S | | | 20 | 1 | 5 0.5 | | |
| 7.4 | 3 F | 2 | 0.4 | 70 | 2 | 5 0.1 | | |
| 7.5 | 5 F | 1 | 0.1 | 100 | 5 | | | |



T and W for Example 4

| <u>Job</u> | <u>Run</u> | <u>Arrives</u> | <u>Finish</u> | <u>Ti</u> | <u>WTi</u> |
|------------|------------|----------------|---------------|-----------|------------|
| 1 | 0.5 | 1.0 | 2.0 | 1.0 | 2.0 |
| 2 | 1.0 | 1.2 | 4.05 | 2.85 | 2.85 |
| 3 | 1.5 | 1.3 | 7.4 | 6.1 | 4.06 |
| 4 | 2.0 | 1.4 | 6.6 | 5.2 | 2.6 |
| 5 | 0.5 | 1.7 | 7.5 | 5.8 | 11.6 |
| 6 | 2.1 | 2.1 | 5.1 | 3.0 | 3.0 |
| | | | | 23.95 | 26.11 |

T = 3.99 WT = 4.35