





What is a thread?	Processes 4
In practice, what's needed to run code "execution stream in an execution <i>execution stream</i> : sequence of ins	on a CPU. context" structions
CPU execution context (1 thread) state: stack, heap, registers position: PC	add \$s0, \$s1, \$s3 sub \$s0, \$s4, \$s0 sw \$s0, 12(\$t7)
OS execution context (in threads) identity + open file descriptors, V	M page table,
Computer Science Dept Va Tech September 2006 Operati	ng Systems ©2006 McQuain & Ribbens



A process is not a program	Processes 6
program: code + data, passive	process: program in execution
int foo;	may execute multiple instances of a single program at once
int main() {	stack
<pre>printf("foo");</pre>	
}	heap data instr
Computer Science Dept Va Tech September 2006 Oper-	ating Systems ©2006 McQuain & Ribbens



Unix		Processes 8	
Fork clones a process.			
Exec overlays the current	Exec overlays the current process.		
No create! Fork, then exec.			
if ((pid = f	fork()) == 0) {		
// this is	child process		
exec();	<pre>// exec does not retur</pre>	n	
}			
else {			
// this is	parent		
wait(pid); // wait for child t	o finish	
}			
Pros: simple, clean			
Cons: duplicate operations			
Computer Science Dept Va Tech September 2006	Operating Systems	©2006. McQuain & Ribbens	

Process environments	Processes 9	
uniprogramming: one process at a ti	me	
"cooperative timesharing"		
mostly PCs, vintage OSes		
easy for OS, usually hard for use	er	
violates isolation		
when should process yield?		
uniprogramming != uniprocessir	ng	
<i>multiprogramming</i> : > 1 process at a	time	
time-sharing		
CTSS, Multics, Unix, VMS, NT,		
multiprogramming != multiproc	essing	
Computer Science Dept Va Tech September 2006 Oper	ating Systems ©2006 McQuain & Ribbens	











	How to switch contexts?	Processes 15	
	Very machine dependent must save		
	general-purpose and FP registers, any co-processor state,		
	Tricky		
	OS code must save state without changing any state.		
	How to run w/o touching any registers? Some CISC machines have single instructions to save all registers on the stack		
	RISC machines reserve some registers for the kernel, or carefully save one and then continue	have a way to	
	How expensive? direct cost of saving + indirect cost of flushing user caches		
(Computer Science Dept Va Tech September 2006 Operating Systems	©2006 McQuain & Ribbens	







	Synchronous thread switching	Processes 19
	Switch threads: #(Arguments: CurrentThread, NextThread)	
	# Save caller's register state.	
	# Save current stack pointer to old thread's stack.	
	# Restore stack pointer for new thread's stack.	
	# Restore caller's register state.	
	# Return	
·	Computer Science Dept Va Tech September 2006 Operating Systems	©2006 McQuain & Ribbens



Process vs threads	Processes 21	
Different address space		
switch page table, etc.		
problems: how to share data? how to communicate?		
Different processes have different privileges		
switch OS's idea of who's running		
Protection		
have to save state in safe place		
need support to forcibly revoke processor, prevent imposters		
Different than procedures?		
OS, not compiler manages state saving		
Computer Science Dept Va Tech September 2006 Operating Systems	©2006 McQuain & Ribbens	

Real OS permutations		Processes 22
One or many address spaces	?	
One or many threads per add		
# address spaces:		
# threads per space	1	many
# uneaus per space		
1	MS/DOS Macintosh	traditional Unix
many	embedded systems Pilot	VMS, Mach, OS2 NT, Solaris, OS X, Linux
		-
Computer Science Dept Va Tech September 2006	Operating Systems	©2006 McQuain & Ribbens

Generic abst	raction template	Processes 23
abstraction:	how OS abstracts underlying resource	
virtualization:	how OS makes small number of resources see number	em like an infinite
partitioning:	how OS divides resources	
protection:	how OS prevents bad people from using piece	es they shouldn't
sharing:	how different instances are shared	
speed:	how OS reduces management overhead	
Computer Science Dept Va Tech Septe	Imber 2006 Operating Systems	©2006 McQuain & Ribbens

How the CPL	J is abstracted	Processes 24	
CPU state repre	CPU state represented as a process		
Virtualization:	processes interleaved transparently		
Partitioning:	CPU shared across time		
Protection:	pigs are forcibly interrupted, process's state identities are protected	saved in OS space,	
Sharing:	yield your CPU time slice to another proces	S	
Speed:	Speed: large scheduling quanta, minimize state needed to switch, share common state (code), duplicate state lazily		
Computer Science Dept Va Tech Septe	mber 2006 Operating Systems	©2006 McQuain & Ribbens	