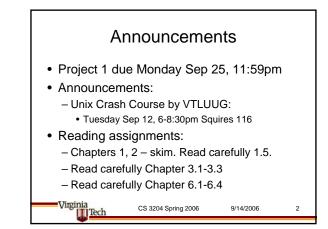
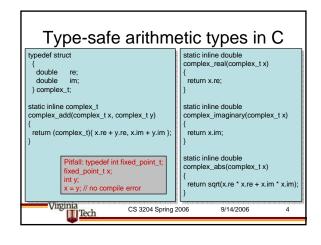
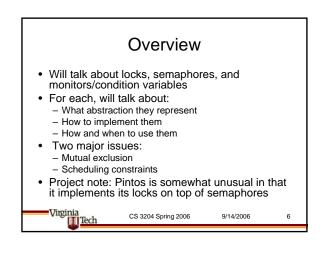
CS 3204 Operating Systems Lecture 7 Godmar Back Virginia

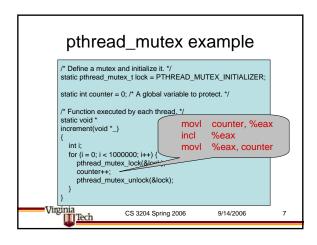


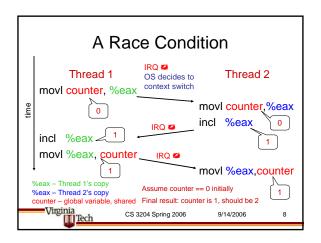
Project 1 Suggested Timeline Today Sep 12: Should have finished alarm clock, implemented basic priority scheduling & should pass almost all alarm & basic priority tests Complete basic priority by Sep 13 Priority Inheritance & Advanced Scheduler take the most time, start them in parallel — will take the most time to implement & debug Due date Sep 25



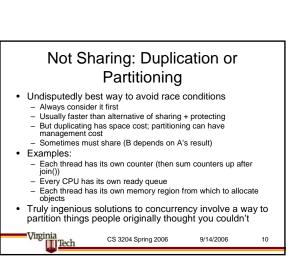
Concurrency & Synchronization Virginia Tech

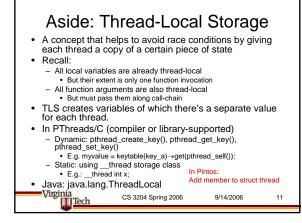






Pace Conditions • Definition: two or more threads read and write a shared variable, and final result depends on the order of the execution of those threads • Usually timing-dependent and intermittent - Hard to debug Not a race condition if all execution orderings lead to same result - Chances are high that you misjudge this • How to deal with race conditions: - Ignore (!?) • Can be ok if final result does not need to be accurate • Never an option in CS 3204 - Don't share: duplicate or partition state - Avoid "bad interleavings" that can lead to wrong result





Prevent race conditions by imposing constraints on execution order so the final result is the same regardless of actual execution order That is, exclude "bad" interleavings Specifically: disallow other threads to start updating shared variables while one thread is in the middle of doing so; make those updates atomic. Virginia CS 3204 Spring 2006 9/14/2006 12



- · Atomic: indivisible
- Certain machine instructions are atomic
- Critical Section
 - A synchronization technique to ensure atomic execution of a segment of code
- Requires entry() and exit() operations

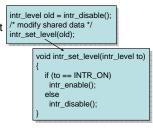


Critical Sections (cont'd)

- Critical Section Problem also known as mutual exclusion
- Only one thread can be inside critical section; others attempting to enter CS must wait until thread that's inside CS leaves it.
- Note: different from "all-or-nothing" meaning atomic has in database theory & practice
 - Does not necessarily imply that thread executes section without interruption, or even that thread completes section – just that other threads can't enter it while one thread is inside it
- Solutions can be entirely software, or entirely hardware
 - Usually combined
- Different solutions for uniprocessor vs multiprocessor scenarios Virginia TTTech CS 3204 Spring 2006 9/14/2006

Disabling Interrupts

- All asynchronous context switches start with interrupts
 - So disable interrupts to avoid them!



Virginia Tech

CS 3204 Spring 2006

9/14/2006

15

Disabling Interrupts: Summary

- (this applies to all variations)
- · Sledgehammer solution
- · Infinite loop means machine locks up
- Use this to protect data structures from concurrent access by interrupt handlers
 - Keep sections of code where irqs are disabled minimal (nothing else can happen until irqs are reenabled - latency penalty!)
 - If you block (give up CPU) mutual exclusion with other threads is not guaranteed
 • Any function that transitively calls thread_block() may block
- Want something more fine-grained
 - Key insight: don't exclude everybody else, only those contending

Virginia Tech

CS 3204 Spring 2006

9/14/2006