

Process Management Tasks

- Define & implement the essential characteristics of a process and thread
 - Algorithms to define the behavior
 - Data structures to preserve the state of the execution
- Define what "things" threads in the process can reference the *address space* (most of the "things" are memory locations)
- Manage the resources used by the processes/threads
- Tools to create/destroy/manipulate processes &

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Process management (...ctd)

Tools to time-multiplex the CPU – Scheduling the (Chapter 7)

■ Mechanisms to handle deadlock (Chapter 10)

Tools to allow threads to synchronize the operation with one another (Chapters 8-9)

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Introduction

- Scenario
 - One process running
 - One/more process performing I/O
 - One/more process waiting on resources
- Most of the complexity stems from the need to manage multiple processes

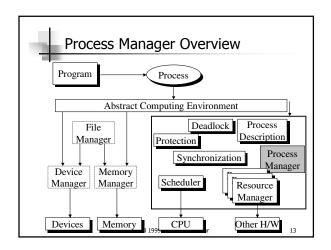
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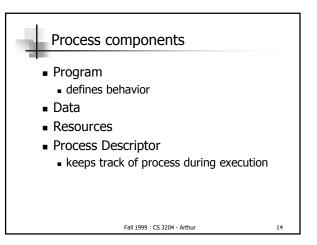


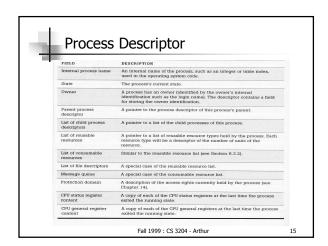
Introduction

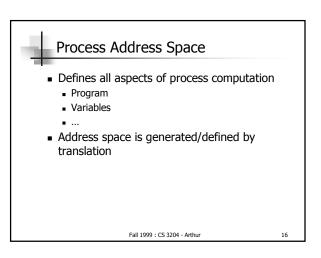
- **Process Manager**
 - CPU sharing
- Process synchronization
- Deadlock prevention

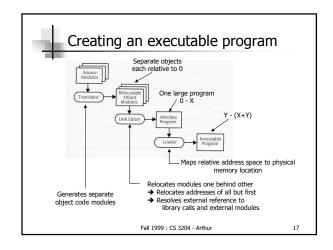
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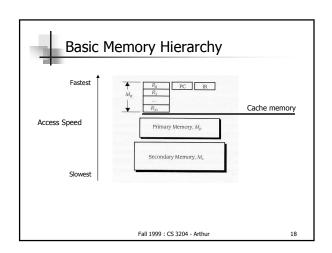














Basic Memory Hierarchy...

At any point in the same program, element can be in

 M_R

- Secondary memory M_S
- Primary memory М
- Registers
- <u>Consistency</u> is a Problem
- - $M_S \neq M_P \neq M_R$ (code vs data) • When does one make them consistent ?

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Consistency Problem

- Scheduler switching out processes Context Switch
- Is Instruction a Problem ???
 - NO
 - Instructions are never modified
 - Separate Instruction and Data space
 - Therefore, $M_{R_i} = M_{P_i} = M_{S_i}$

How can an instruction be in a register?

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Consistency Problem...

- Is Data a Problem ???
 - YES
 - Variable temporarily stored in register has value added to it
 - Therefore, M_{Rj} ≠ M_{Pj}
- On context switch, all registers are saved
 - Therefore, current state is saved

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Sample Scenario...

- Suppose 'MOV X Y' instruction is executed
 - $\rightarrow M_{P_y} \neq M_{S_y}$
- On context switch, is all of a process' memory flushed to M_S?
 - No, only on page swap
- Hence, $env_{process} = (M_R + M_S) + (...)$
- - Flushing of memory frees it up for incoming process => Page Swap

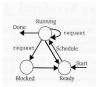
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Process States

- Focus on Resource Management & Process Management
- Recall also that part of the process environment is its state

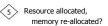


State Transition Diagram

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Process States...

- When process enters 'Ready' state, it must compete for CPU. Memory has already been allocated
- Process has CPU
- Process requests resource that is $\underline{immediately}$ available \rightarrow NO blocking
 - Process requests resource that is $\underline{\mathsf{NOT}}$ yet



State Transition Diagram

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