Chapter 6



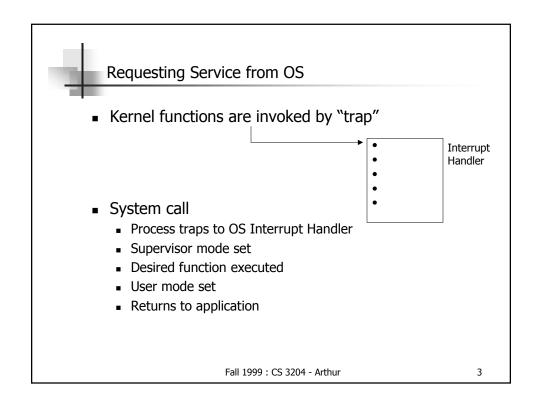
Process Management

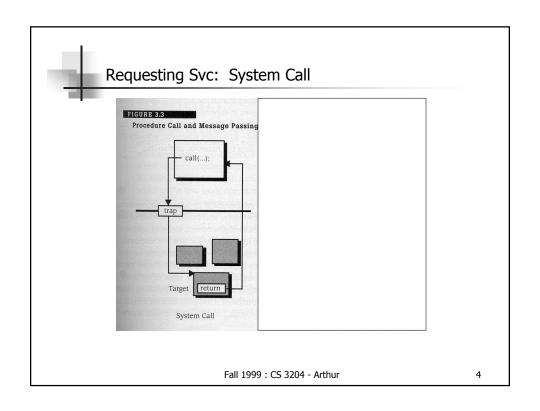


Last lecture review

- Von Neumann computer comprises of
 - CPU (ALU + Control Unit)
 - Memory Unit
 - Devices
 - Bus
- Boot-strapping
- Interrupts and interrupt handling
- Trap mechanism (more explanation today)

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Revisiting the trap Instruction (H/W)

```
executeTrap(argument) {
    setMode(supervisor);
    switch(argument) {
    case 1: PC = memory[1001]; // Trap handler 1
    case 2: PC = memory[1002]; // Trap handler 2
    . . .
    case n: PC = memory[1000+n];// Trap handler n
};
```

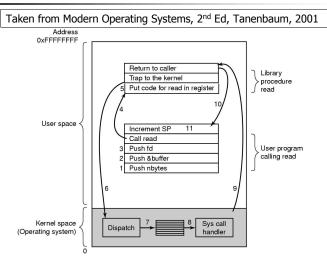
- The trap instruction dispatches a trap handler routine atomically
- Trap handler performs desired processing
- "A trap is a software interrupt"

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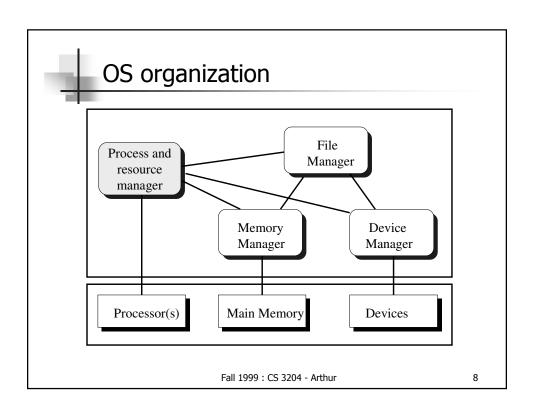
Steps in making a system call



There are 11 steps in making the system call read (fd, buffer, nbytes)

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Process Management





Process Management Tasks

- Define & implement the essential characteristics of a process and thread
 - Algorithms to define the behavior
 - Data structures to preserve the state of the execution
- Define what "things" threads in the process can reference – the address space (most of the "things" are memory locations)
- Manage the resources used by the processes/threads
- Tools to create/destroy/manipulate processes & threads

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Process management (...ctd)

- Tools to time-multiplex the CPU Scheduling the (Chapter 7)
- Tools to allow threads to synchronize the operation with one another (Chapters 8-9)
- Mechanisms to handle deadlock (Chapter 10)

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Introduction

- Scenario
 - One process running
 - One/more process performing I/O
 - One/more process waiting on resources
- Most of the complexity stems from the need to manage multiple processes

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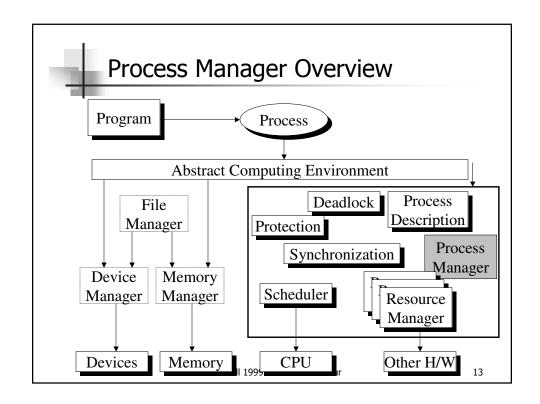
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Introduction

- Process Manager
 - CPU sharing
 - Process synchronization
 - Deadlock prevention

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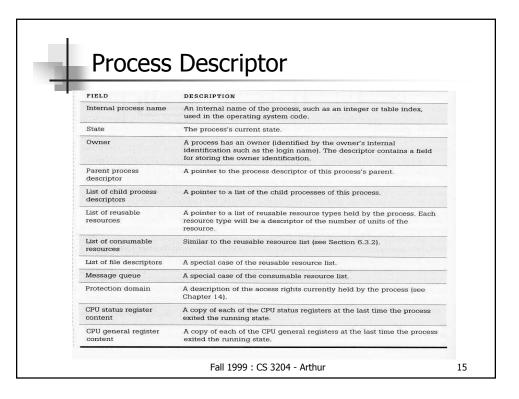




Process components

- Program
 - defines behavior
- Data
- Resources
- Process Descriptor
 - keeps track of process during execution

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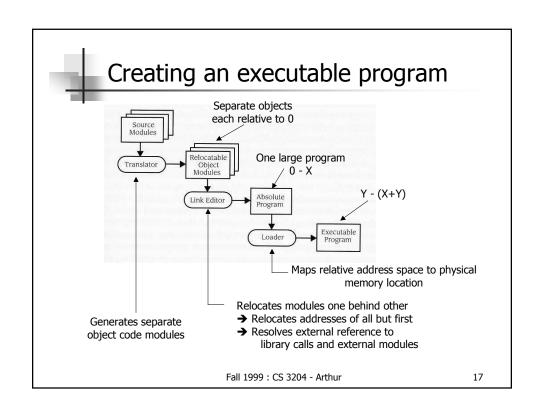


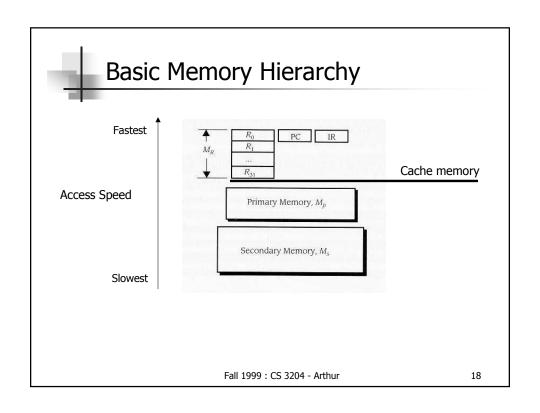


Process Address Space

- Defines all aspects of process computation
 - Program
 - Variables
 - **=** ...
- Address space is generated/defined by translation

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Basic Memory Hierarchy...

- At any point in the same program, element can be in
 - Secondary memory M_S Primary memory M_P Registers M_R
- Consistency is a Problem
 - $M_S \neq M_P \neq M_R$ (code vs data)
 - When does one make them consistent ?
 - How ?

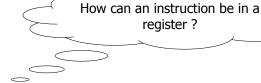
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Consistency Problem

- Scheduler switching out processes Context Switch
- Is Instruction a Problem ???
 - NO
 - Instructions are never modified
 - Separate Instruction and Data space
 - Therefore, $M_{R_j} = M_{P_j} = M_{S_j}$



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register?



Consistency Problem...

- Is Data a Problem ???
 - YES
 - Variable temporarily stored in register has value added to it
 - Therefore, M_{Ri} ≠ M_{Pi}
- On context switch, all registers are saved
 - Therefore, current state is saved

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Sample Scenario...

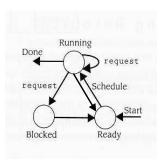
- Suppose 'MOV X Y' instruction is executed
 - $\bullet \hspace{0.1in} \bullet \hspace{0.1in} M_{P_y} \neq M_{s_y}$
- On context switch, is all of a process' memory flushed to M_S?
 - No, only on page swap
- Hence, $env_{process} = (M_R + M_S) + (...)$
- Note:
 - Flushing of memory frees it up for incoming process> Page Swap

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Process States

- Focus on Resource
 Management & Process
 Management
- Recall also that part of the process environment is its state



State Transition Diagram

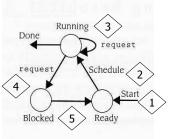
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Process States...

- When process enters 'Ready' state, it must compete for CPU. Memory has already been allocated
- 2 Process has CPU
- Process requests resource that is immediately available →NO blocking
- 4 Process requests resource that is <u>NOT</u> yet available
- Resource allocated, memory re-allocated?



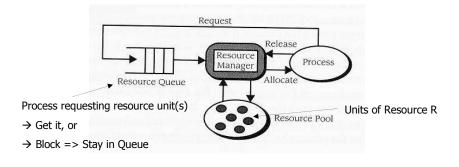
State Transition Diagram

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Resources & Resource Manager

- 2 types of Resources
 - Reusable (Memory)
 - Consumable (Input/Time slice)



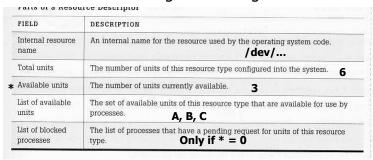
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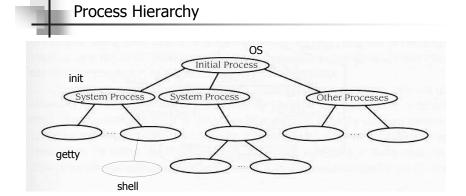


Resource Descriptor

- Each Resource R has a Resource Descriptor associated with it (similar to the process)
 - => there is a "Status" for that Resource, and
 - => a Resource Manager to manage it



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- Conceptually, this is the way in which we would like to view it
- Root controls all processes i.e. Parent

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Creating Processes

- Parent Process needs ability to
 - Block child
 - Activate child
 - Destroy child
 - Allocate resources to child
- True for User processes spawning child
- True for OS spawning init, getty, etc.
- Process hierarchy a natural,

if fork/exec commands exist

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UNIX fork command

- Forkunix
 - Shares text
 - Shares memory
 - Has its own address space
 - Cannot communicate with parent by referring variable stored in code
- Earlier definition: ForkConway
 - Shares text
 - Shares resources
 - Shares address space
 - Process can communicate thru variables declared in code

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```
Prog
               proc_A(){
                                                   proc_B(){
                   while(TRUE) {
x, y:int
                                                       while(TRUE) (
                       <compute section A1>;
                                                           retrieve(x);
 Proc A
                       update(x);
   ref x & y
                                                           ⟨compute section B1⟩;
                       ⟨compute section A2⟩;
 Proc B
                                                           update(y);
   ref x & y
                       retrieve(y);
                                                          ⟨compute section B2⟩;
Fork "A"
Fork "B"
```

Now processes A & B, share address space & can communicate thru declared variables

Problem ???

A can write 2 times before B reads

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Synchronizing Access to Shared Variables

- Shared address space allows communication through declared variables <u>automatically</u>
- How then, can we synchronize access to them?
- x, y : int

 Porc A
 ref x & y

 Proc B
 ref x & y

 Fork "A"

 Fork "B"

Prog

- Need Sychronization Primitives
 - => JOIN & QUIT

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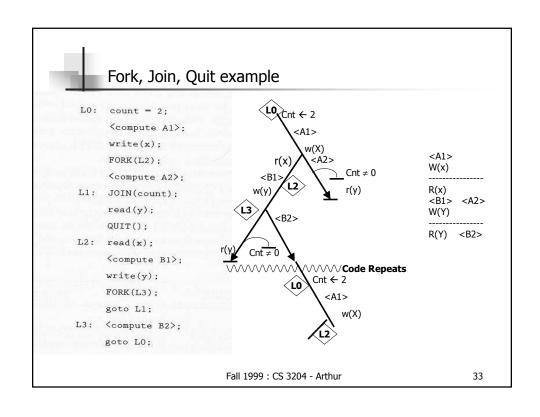
Fork, Join & Quit - Conway

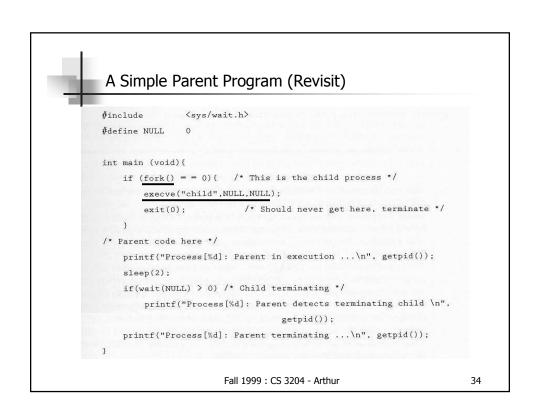
- In addition to the "Fork(proc)" command, Conway also defined system calls to support process synchronization
- Join (count)
 - Un-interruptable

Decrement count; if count ≠ 0 then Quit, else Continue

- Quit
 - Terminate process

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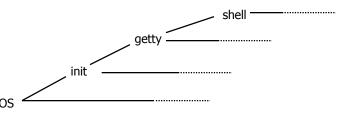


Spawning A Child Different From Parent

 Suppose we wish to spawn a child that is <u>different</u> from the parent

fork execve(...)





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Factoring in additional Control Complexities

- Recall:
 - A parent process can <u>suspend</u> a child process
- Therefore, if a child is in <u>run</u> state and goes to ready (time slice up), and the parent runs and decides to suspend the child, then how do we reflect this in the process state diagram ???
- We need 2 more states
 - Ready suspended
 - Blocked suspended

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