

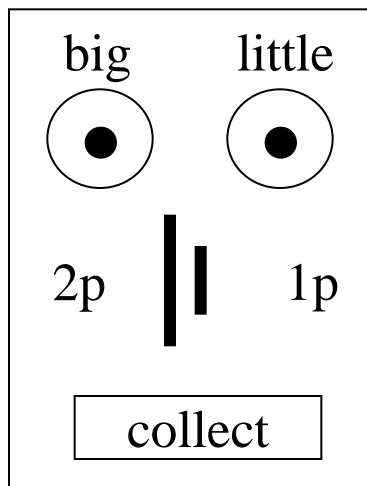


# $\pi$ Calculus

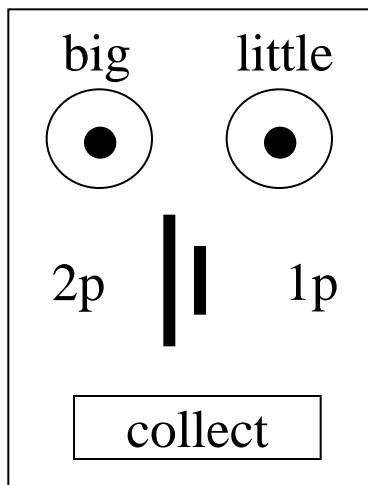
Reasoning about concurrency and communication (Part 2).

## A Process with Alternative Behavior

A vending machine that dispenses chocolate candies allows either a 1p (p for pence) or a 2p coin to be inserted. After inserting a 1p coin, a button labelled “little” may be pressed and the machine will then dispense a small chocolate. After inserting a 2p coin, the “big” button may be pressed and the machine will then dispense a large chocolate. The candy must be collected before additional coins can be inserted.



# An Process with Alternative Behavior



$$\begin{aligned} \text{VM}(\text{big}, \text{little}, \text{collect}, 1p, 2p) = & \\ & 2p.\overline{\text{big}}.\overline{\text{collect}} \text{largeChoc}.\text{VM}(\text{big}, \text{little}, \text{collect}, 1p, 2p) \\ & + 1p.\overline{\text{little}}.\overline{\text{collect}} \text{smallChoc}.\text{VM}(\text{big}, \text{little}, \text{collect}, 1p, 2p) \end{aligned}$$

The plus (“+”) operator expresses alternative behavior.

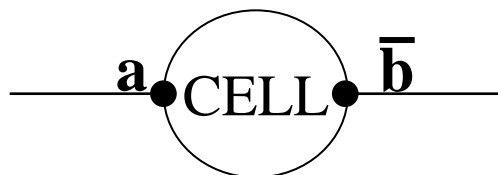
## Modeling a Bounded Buffer

Suppose that a buffer has get and put operations and can hold up to three data items. Ignoring the content of the data items, and focusing only on the operations, a buffer can be defined as:

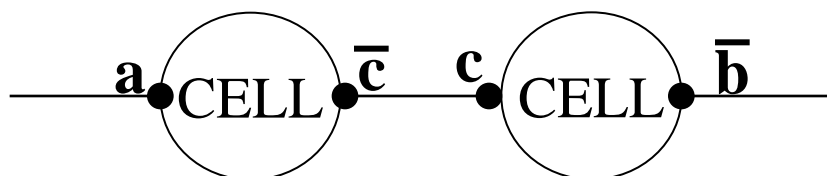
$$\begin{aligned} \text{Buffer}_0(\text{put}, \text{get}) &= \text{put}.\text{Buffer}_1(\text{put}, \text{get}) \\ \text{Buffer}_1(\text{put}, \text{get}) &= \text{put}.\text{Buffer}_2(\text{put}, \text{get}) + \overline{\text{get}}.\text{Buffer}_0(\text{put}, \text{get}) \\ \text{Buffer}_2(\text{put}, \text{get}) &= \text{put}.\text{Buffer}_3(\text{put}, \text{get}) + \overline{\text{get}}.\text{Buffer}_1(\text{put}, \text{get}) \\ \text{Buffer}_3(\text{put}, \text{get}) &= \overline{\text{get}}.\text{Buffer}_2(\text{put}, \text{get}) \end{aligned}$$

Notice that this captures the idea that a get operation is not possible when the buffer is empty (i.e., in state  $\text{Buffer}_0$ ) and a put operation is not possible when the buffer is full (i.e., in state  $\text{Buffer}_3$ ).

## Reusing a Process Definition



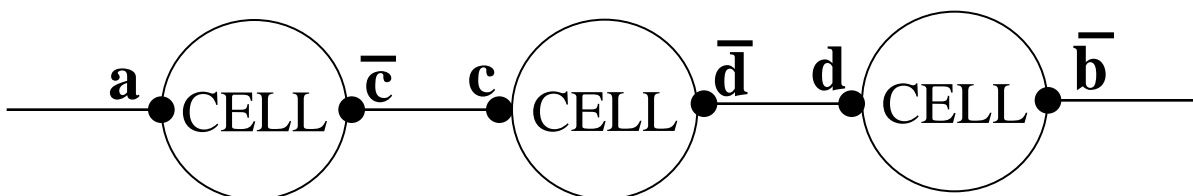
$$\text{CELL}(a,b) = a.\bar{b}.\text{CELL}(a,b)$$



$$C0 = \text{CELL}(a, c)$$

$$C1 = \text{CELL}(c, b)$$

$$\text{BUFF2} = (\nu c) ( C0 \mid C1 )$$



$$C0 = \text{CELL}(a,c)$$

$$C1 = \text{CELL}(c,d)$$

$$C2 = \text{CELL}(d,b)$$

$$\text{BUFF3} = (\nu c)(\nu d)( C0 \mid C1 \mid C2 )$$

# Modeling Mutual Exclusion

A lock to control access to a critical region is modeled by:

$$\begin{aligned} \text{Lock}(\text{lock}, \text{unlock}) &= \text{lock}.\text{Locked}(\text{lock}, \text{unlock}) \\ \text{Locked}(\text{lock}, \text{unlock}) &= \text{unlock}.\text{Lock}(\text{lock}, \text{unlock}) \end{aligned}$$

A generic process with a critical region follows the locking protocol is:

$$\begin{aligned} \text{Process}(\text{enter}, \text{exit}, \text{lock}, \text{unlock}) \\ = \overline{\text{lock}}.\text{enter}.\text{exit}.\overline{\text{unlock}}.\text{Process}(\text{enter}, \text{exit}, \text{lock}, \text{unlock}) \end{aligned}$$

A system of two processes is:

$$\begin{aligned} \text{Process}_1 &= \text{Process}(\text{enter}_1, \text{exit}_1, \text{lock}, \text{unlock}) \\ \text{Process}_2 &= \text{Process}(\text{enter}_2, \text{exit}_2, \text{lock}, \text{unlock}) \\ \text{MutexSystem} &= (\nu \text{lock}) (\nu \text{unlock}) (\text{Process}_1 \mid \text{Process}_2 \mid \text{Lock}) \end{aligned}$$

# Modeling Mutual Exclusion

A system of two processes is:

$$\text{Process}_1 = \text{Process}(\text{enter}_1, \text{exit}_1, \text{lock}, \text{unlock})$$
$$\text{Process}_2 = \text{Process}(\text{enter}_2, \text{exit}_2, \text{lock}, \text{unlock})$$
$$\text{MutexSystem} = \text{new lock, unlock}(\text{Process}_1 \mid \text{Process}_2 \mid \text{Lock})$$

A “specification” for this system is:

$$\text{MutexSpec}(\text{enter}_1, \text{exit}_1, \text{enter}_2, \text{exit}_2)$$
$$= \text{enter}_1.\text{exit}_1.\text{MutexSpec}(\text{enter}_1, \text{exit}_1, \text{enter}_2, \text{exit}_2)$$
$$+ \text{enter}_2.\text{exit}_2.\text{MutexSpec}(\text{enter}_1, \text{exit}_1, \text{enter}_2, \text{exit}_2)$$

## Modeling a Bounded Buffer

The Buffer equations might be thought of as the “specification” of the bounded buffer because it only refers to states of the buffer and not to any internal components or machinery to create these states.

An “implementation” of the bounded buffer is readily available by re-labeling the BUFF3 agent developed earlier

$$\text{CELL} = a.\bar{b}.\text{CELL}$$

$$\text{C0} = \text{CELL} (\text{put} , c)$$

$$\text{C1} = \text{CELL} (c , d)$$

$$\text{C2} = \text{CELL} (d , \text{get})$$

$$\text{BufferImpl} = (\nu c) (\nu d) ( \text{C0} \mid \text{C1} \mid \text{C2} )$$



## Equality of Processes

We would like to know if two process have the same behavior (interchagable), or if an implementation has the behavior required by a given specification (conformance). For example:

is  $\text{Buffer}_0 = \text{BufferImpl}$  ?

is  $\text{MutexSystem} = \text{MutexSpec}$  ?

How do we tell if two behaviors are the same?

# Structural Congruence

Two expressions are the same if one can be transformed to the other using these rules:

(1) change of bound names :  $(\nu a) (a.P) = (\nu c) (c.P)$

(2) reordering of terms in summation:  $a.P + b.Q = b.Q + a.P$

(3)  $P \mid 0 = P$ ,  $P \mid Q = Q \mid P$ ,  $P \mid (Q \mid R) = (P \mid Q) \mid R$

(4)  $(\nu x) (P \mid Q) = P \mid (\nu x) Q$  if  $x$  is not a free name in  $P$ ,  
 $(\nu x) 0 = 0$ ,  $(\nu x) (\nu y) P = (\nu y) (\nu x) P$

# Reaction Rules

An equation can be changed by the application of these rules that express the “reaction” of the system being described:

$$\text{COMM: } (x(y).P + M) \mid \bar{x} z.Q + N \rightarrow \{z/y\}P \mid Q$$

$$\text{PAR: } \frac{P \rightarrow P'}{P \mid Q \rightarrow P' \mid Q}$$

$$\text{RES: } \frac{P \rightarrow P'}{(\nu x) P \rightarrow (\nu x) P'}$$

$$\text{STRUCT: } \frac{Q=P \quad P \rightarrow P' \quad P'=Q'}{Q \rightarrow Q'}$$

# Reaction Rules

Processes:  $A(a,c) = a.A'(a,c)$        $B(c,b) = c.B'(c,b)$

$A'(a,c) = \bar{c}.A(a,c)$        $B'(c,b) = \bar{b}.B(c,b)$

A system:  $\text{System} = \nu c (A | B)$

Show:  $\nu c (A' | B) \longrightarrow \nu c (A | B')$

by REACT:  $\bar{c}.A | c.B' \longrightarrow A | B'$

by RES:  $\nu c (c.\bar{A} | c.B') \longrightarrow \nu c (A | B')$

by definition:  $\nu c (A' | B) \longrightarrow \nu c (A | B')$

## Depicting an Agent's Behavior

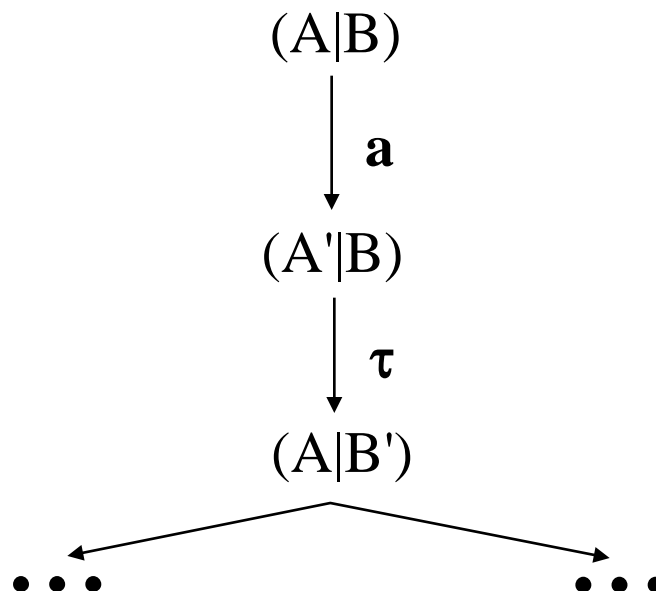
Define:

$$A = a.A' \quad B = c.B'$$

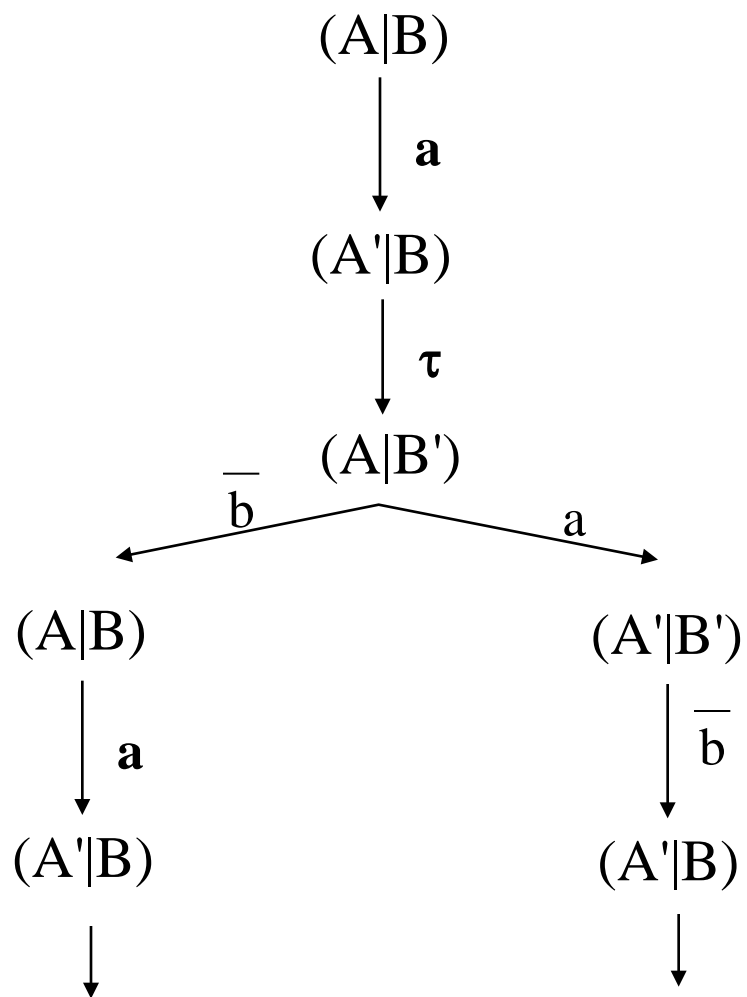
$$A' = \bar{c}.A \quad B' = b.B \quad \text{---}$$

$$\text{System} = (\nu c) (A \mid B)$$

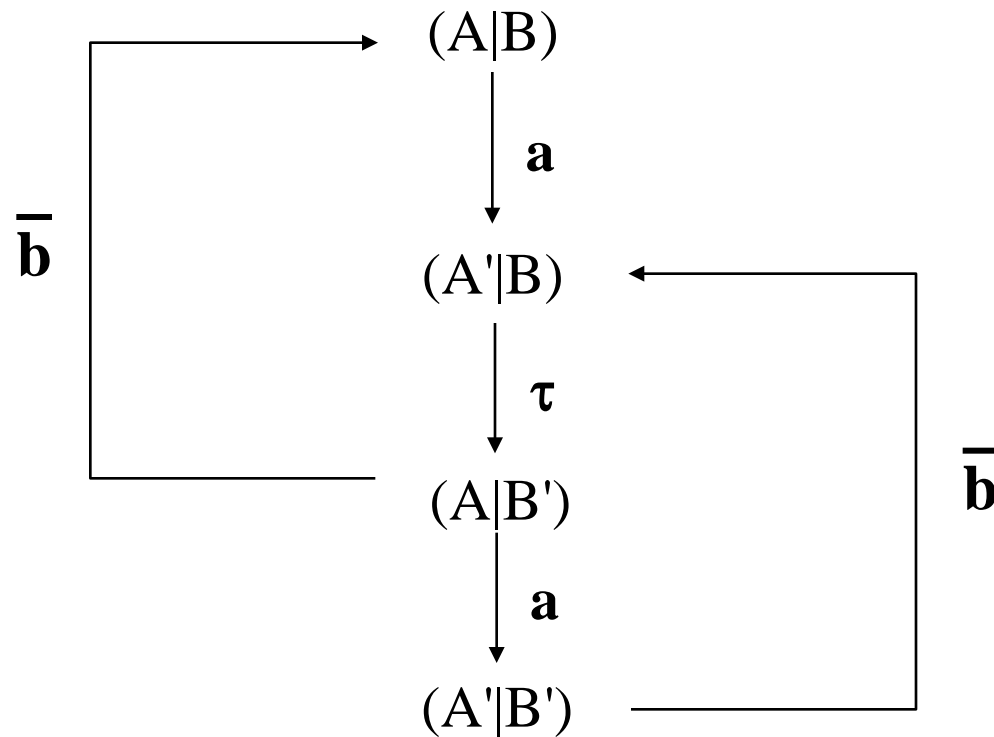
Draw a graph to show all possible sequences of actions. Here is the start:



## More of the Behavior



## Depicting an Agent's Behavior



## Equivalence of Agents

$$A \stackrel{\text{def}}{=} a.A_1$$

$$B \stackrel{\text{def}}{=} a.B_1 + a.B'_1$$

$$A_1 \stackrel{\text{def}}{=} b.A_2 + c.A_3$$

$$B_1 \stackrel{\text{def}}{=} b.B_2$$

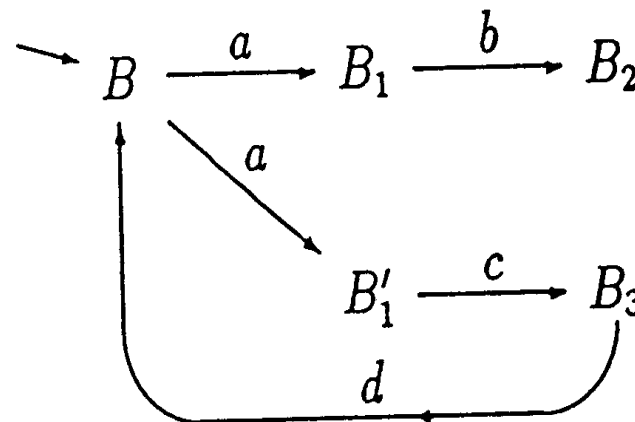
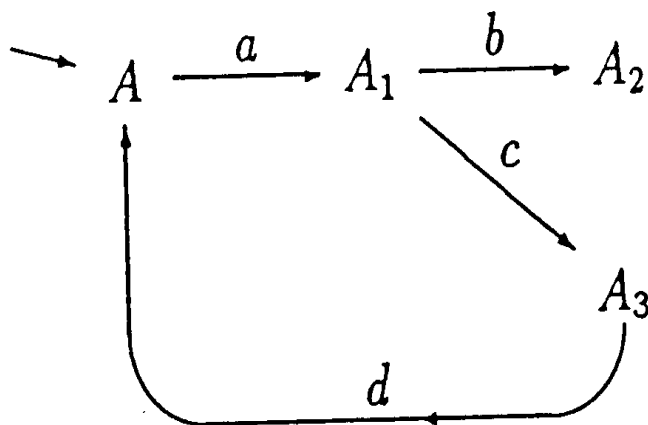
$$B'_1 \stackrel{\text{def}}{=} c.B_3$$

$$A_2 \stackrel{\text{def}}{=} 0$$

$$B_2 \stackrel{\text{def}}{=} 0$$

$$A_3 \stackrel{\text{def}}{=} d.A$$

$$B_3 \stackrel{\text{def}}{=} d.B$$





# Bisimulation

The behavior of two process are equal when each can simulate exactly the behavior of the other.

